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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Wednesday 06 December 2006

**FPGA Development Platform. Design Examples - Adriatico Guest House Informatics Lab.
(16:00-18:00)**

time	title	presenter
16:00	FPGA Development Platform. Design Examples	ALEXANDER SHAPIRO