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ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Friday 08 December 2006

Laboratory projects: Proposals and discussion - Adriatico Guest House Giambiagi Lecture Hall (09:30-10:30)

time	title	presenter
09:30	Laboratory projects: Proposals and discussion	MARIA LIZ CRESPO