ICTP-INFN Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 1780)

Contribution ID: 54 Type: not specified

Laboratory projects: Proposals and discussion (cont)

Monday, 11 December 2006 17:00 (2:00)

Content

Summary

Primary author(s): MARIA LIZ CRESPO

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Session Classification: Laboratory projects: Proposals and discussion (cont)