Advanced School in High Performance and GRID Computing | (smr 1967)

Contribution ID :  $\bf 62$ 

Type : not specified

## Queue systems for HPC platform

Tuesday, 11 November 2008 09:00 (0:45)

Content

Summary

**Primary author(s) :** PIERO CALUCCI (S.I.S.S.A. - International School for Advanced Studies, Trieste, Italy)

**Presenter(s) :** PIERO CALUCCI (S.I.S.S.A. - International School for Advanced Studies, Trieste, Italy)

Session Classification : Queue systems for HPC platform