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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 01 July 2008

Models, Mechanics, and Opportunities for Collaborative Engineering and Design in the New Cyber Age. II (11:00-12:00)

time	title	presenter
11:00	Models, Mechanics, and Opportunities for Collaborative Engineering and Design in the New Cyber Age. II	KHAN JAVED IQBAL