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First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 01 July 2008

Reconfigurable Virtual Instrumentation (RVI) based on FPGA (cont.) (15:00-16:00)

time	title	presenter
15:00	Reconfigurable Virtual Instrumentation (RVI) based on FPGA (cont.)	ANDRES CICUTTIN