## **Table of contents**

Wednesday 02 July 2008	
Wednesday of July 2000	

## First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

## Wednesday 02 July 2008

Two-dimensional digital signal processing I. (11:00-12:00)

time title	presenter	
11:00 Two-dimensional digital signal processing I.	FABIO MAMMANO	