

## **Laboratory Session. RVI Projects. Digital Signal Processing with FPGA.**

*Friday, 4 July 2008 16:30 (2:30)*

### **Content**

### **Summary**

**Primary author(s) :** MARIA LIZ CRESPO

**Presenter(s) :** MARIA LIZ CRESPO

**Session Classification :** Laboratory Session. RVI Projects. Digital Signal Processing with FPGA.