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# **First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)**

**Tuesday 08 July 2008**

## **Formal Verification Techniques for FPGA (09:30-11:00)**

time	title	presenter
09:30	Formal Verification Techniques for FPGA	ELISHA LYE