



# Table of contents

Thursday 10 July 2008 .....	1
-----------------------------	---

# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

## Thursday 10 July 2008

### Challenges in Low Power Design (16:30-17:30)

time	title	presenter
16:30	Challenges in Low Power Design	JASMINE NG