



# Table of contents

Monday 16 June 2008 .....	1
---------------------------	---

# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

**Monday 16 June 2008**

**Break (10:15-10:30)**

time	title	presenter
10:15	Break	