



# Table of contents

Friday 20 June 2008 .....	1
---------------------------	---

# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

## Friday 20 June 2008

### Lunch (13:00-15:00)

time	title	presenter
13:00	Lunch	