



# Table of contents

Monday 23 June 2008 .....	1
---------------------------	---

# First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

**Monday 23 June 2008**

**Dinner (20:00-22:00)**

time	title	presenter
20:00	Dinner	