

Table of contents

Tuesday 24 June 2008	1
----------------------------	---

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Tuesday 24 June 2008

Actel Flash FPGA architecture (09:30-10:30)

time	title	presenter
09:30	Actel Flash FPGA architecture	NIZAR ABDALLAH