

Table of contents

Wednesday 25 June 2008	1
------------------------------	---

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Wednesday 25 June 2008

Advance FPGA applications. A case study in HEP experiments (12:00-13:00)

time	title	presenter
12:00	Advance FPGA applications. A case study in HEP experiments	ALEXANDER KLUGE