

Table of contents

Monday 16 June 2008	1
---------------------------	---

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Monday 16 June 2008

Effective FPGA/VLSI Education Techniques (15:30-16:30)

time	title	presenter
15:30	Effective FPGA/VLSI Education Techniques	STEPHEN BROWN