Table of contents

i

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977) / Programme

Thursday 26 June 2008

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Thursday 26 June 2008

Fourier Theory I (11:00-12:00)

time	title	presenter
11:00	Fourier Theory I	MARCELO MAGNASCO

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977) / Programme

Thursday 26 June 2008

Page 1