

Table of contents

| | |
|-----------------------------|---|
| Thursday 26 June 2008 | 1 |
|-----------------------------|---|

First ICTP Regional Microelectronics Workshop and Training on VHDL for Hardware Synthesis and FPGA Design in Asia-Pacific | (smr 1977)

Thursday 26 June 2008

Break (16:00-16:30)

| time | title | presenter |
|-------|-------|-----------|
| 16:00 | Break | |