



# Table of contents

Wednesday 11 November 2009 .....	1
----------------------------------	---

# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Wednesday 11 November 2009

**Digital Filters Design - Adriatico Guest House Kastler Lecture Hall (16:00-17:00)**

time	title	presenter
16:00	Digital Filters Design	M. NOLICH