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# **Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)**

## **Wednesday 11 November 2009**

**Laboratory Session. Digital Arithmetic (cont.) - Adriatico Guest House Informatics Lab.**  
**(17:00-18:00)**

time	title	presenter
17:00	Laboratory Session. Digital Arithmetic (cont.)	M. L. CRESPO