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Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Tuesday 27 October 2009

Digital Design II (Sequential elements, Mealy and Moore FSM) - Adriatico Guest House
Kastler Lecture Hall (11:00-12:00)

time	title	presenter
11:00	Digital Design II (Sequential elements, Mealy and Moore FSM)	P. BAZARGAN-SABET