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# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Thursday 12 November 2009

### Laboratory Session: Waveform generation (cont.)

- Adriatico Guest House Informatics Lab. (16:00-18:00)

time	title	presenter
16:00	Laboratory Session: Waveform generation (cont.)	M.L. CRESPO