



# Table of contents

Friday 13 November 2009 .....	1
-------------------------------	---

# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Friday 13 November 2009

**Laboratory Session: ICTP RVI Platform Description - Adriatico Guest House Informatics**

**Lab. (14:30-15:30)**

time	title	presenter
14:30	Laboratory Session: ICTP RVI Platform Description	K.M. KHARE