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# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Monday 16 November 2009

### Three-dimensional deconvolution in FPGA - Adriatico Guest House Kastler Lecture Hall (14:30-15:30)

| time | title | presenter |
|---|---|---|
| 14:30 | Three-dimensional deconvolution in FPGA | F. MAMMANO |