



# Table of contents

Wednesday 18 November 2009 .....	1
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# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Wednesday 18 November 2009

**Laboratory Session. RVI Projects. Digital Signal Processing with FPGA - Adriatico Guest**  
**House Informatics Lab. (09:30-10:30)**

time title

presenter

09:30	Laboratory Session. RVI Projects. Digital Signal Processing with FPGA	
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