

Table of contents

Wednesday 18 November 2009	1
----------------------------------	---

Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Wednesday 18 November 2009

Darwin & Modern Science

- Leonardo da Vinci Building Main Lecture Hall (16:00-18:00)

More details on:

<http://pio.ictp.it/words/news/2009/darwin-and-modern-science>

time	title	presenter
16:00	Darwin & Modern Science	