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Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Tuesday 27 October 2009

(LiberioTM IDE) Design Entry - Adriatico Guest House Kastler Lecture Hall (16:00-17:00)

time	title	presenter
16:00	(LiberioTM IDE) Design Entry	N. ABDALLAH