

Advanced Training Course on FPGA Design and VHDL for Hardware  
Simulation and Synthesis | (smr 2065)

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## **(Liberio™ IDE) Functional Simulation. Synthesis**

*Tuesday, 27 October 2009 17:00 (1:00)*

### **Content**

### **Summary**

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**Session Classification :** (Liberio™ IDE) Functional Simulation. Synthesis