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Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Wednesday 28 October 2009

**Digital arithmetic I (number representations) - Adriatico Guest House Kastler Lecture Hall
(11:00-12:00)**

time	title	presenter
11:00	Digital arithmetic I (number representations)	P. BAZARGAN-SABET