



# Table of contents

Wednesday 28 October 2009 .....	1
---------------------------------	---

# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Wednesday 28 October 2009

### Synthesis III - Advanced VHDL - Adriatico Guest House Kastler Lecture Hall (12:00-13:00)

time	title	presenter
12:00	Synthesis III - Advanced VHDL	N. ABDALLAH