## **Table of contents**

Wednesday 28 October 2009		
---------------------------	--	--

## Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Wednesday 28 October 2009

<u>Design Verification and Timing Concepts</u> - Adriatico Guest House Kastler Lecture Hall (16:00-17:00)

time title	presenter
16:00 Design Verification and Timing Concepts	N. ABDALLAH