

Advanced Training Course on FPGA Design and VHDL for Hardware  
Simulation and Synthesis | (smr 2065)

Contribution ID : 19

Type : **not specified**

## Programmable logic & FPGA architectures

*Thursday, 29 October 2009 11:00 (1:00)*

### Content

### Summary

**Primary author(s)** : N. ABDALLAH

**Presenter(s)** : N. ABDALLAH

**Session Classification** : Programmable logic & FPGA architectures