



# Table of contents

Monday 02 November 2009 .....	1
-------------------------------	---

# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Monday 02 November 2009

**Laboratory Session. Laboratory Session. Finite State Machine: VHDL Description and Simulation (cont.) - Adriatico Guest House Informatics Lab. (16:00-18:00)**

time	title	presenter
16:00	Laboratory Session. Laboratory Session. Finite State Machine: VHDL Description and Simulation (cont.)	M.L. CRESPO