

Advanced Training Course on FPGA Design and VHDL for Hardware  
Simulation and Synthesis | (smr 2065)

Contribution ID : 36

Type : **not specified**

## **Presentation of FPGA Projects by Participants and Discussion**

*Wednesday, 4 November 2009 09:30 (1:00)*

**Content**

**Summary**

**Session Classification :** Presentation of FPGA Projects by Participants and Discussion