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# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Wednesday 04 November 2009

**Presentation of FPGA Projects by Participants and Discussion - Adriatico Guest House  
Kastler Lecture Hall (16:00-18:00)**

time	title	presenter
16:00	Presentation of FPGA Projects by Participants and Discussion	