Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Contribution ID : 40 Type : not specified

Selected Topics on Logic Synthesis and FPGA Debugging

Thursday, 5 November 2009 09:30 (1:00)

Content

Summary

Presenter(s): A. CICUTTIN

Session Classification: Selected Topics on Logic Synthesis and FPGA Debugging