

Table of contents

Thursday 05 November 2009	1
---------------------------------	---

Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Thursday 05 November 2009

**Laboratory Session. Hardware Description of the FPGA Development Platform. - Adriatico
Guest House Informatics Lab. (12:00-13:00)**

time	title	presenter
12:00	Laboratory Session. Hardware Description of the FPGA Development Platform.	C. SOSA PAEZ