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Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Monday 26 October 2009

**Introduction to Actel Prodcuts. Libero IDE Overview and Design Flow - Adriatico Guest
House Kastler Lecture Hall (16:00-17:00)**

time	title	presenter
16:00	Introduction to Actel Prodcuts. Libero IDE Overview and Design Flow	N. ABDALLAH