

Advanced Training Course on FPGA Design and VHDL for Hardware  
Simulation and Synthesis | (smr 2065)

Contribution ID : 43

Type : **not specified**

## Laboratory Session. Hardware Description of the FPGA Development Platform (cont.)

*Thursday, 5 November 2009 14:30 (1:00)*

**Content**

**Summary**

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**Session Classification :** Laboratory Session. Hardware Description of the FPGA Development Platform (cont.)