

Advanced Training Course on FPGA Design and VHDL for Hardware  
Simulation and Synthesis | (smr 2065)

Contribution ID : 44

Type : **not specified**

## Laboratory Session. FPGA Implementation Example.

*Thursday, 5 November 2009 16:00 (2:00)*

### Content

### Summary

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**Session Classification** : Laboratory Session. FPGA Implementation Example.