



# Table of contents

Friday 06 November 2009 .....	1
-------------------------------	---

# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Friday 06 November 2009

**Reconfigurable Virtual Instrumentation (RVI) based on FPGA - Adriatico Guest House**  
**Kastler Lecture Hall (09:30-10:30)**

time	title	presenter
09:30	Reconfigurable Virtual Instrumentation (RVI) based on FPGA	A. CICCUTTIN