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Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Tuesday 10 November 2009

**A case study in HEP experiments (I) - Adriatico Guest House Kastler Lecture Hall
(11:00-13:00)**

time	title	presenter
11:00	A case study in HEP experiments (I)	A. KLUGE