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# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Wednesday 11 November 2009

**A case study in HEP experiments (II) - Adriatico Guest House Kastler Lecture Hall**

**(09:30-10:30)**

time	title	presenter
09:30	A case study in HEP experiments (II)	A. KLUGE