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ICTP Latin-American Basic Course on FPGA Design for Scientific Instrumentation | (smr 2177)

Friday 19 March 2010

Laboratory Exercises: VHDL Behavioral Description and Simulation of Combinational Circuits (cont.) (14:00-15:00)

time	title	presenter
14:00	Laboratory Exercises: VHDL Behavioral Description and Simulation of Combinational Circuits (cont.)	