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ICTP Latin-American Basic Course on FPGA Design for Scientific Instrumentation | (smr 2177)

Wednesday 24 March 2010

Laboratory Exercises: Finite State Machine. Synthesis and Post-Synthesis Simulation (cont.) (15:00-16:00)

time	title	presenter
15:00	Laboratory Exercises: Finite State Machine. Synthesis and Post-Synthesis Simulation (cont.)	