

Hardware Description of FPGA Development Platform: Nexys 2

Thursday, 29 November 2012 13:25 (1:00)

Content

Summary

Primary author(s) : MIGUEL RISCO CASTILLO (Technological University of Peru, Lima , Peru)

Presenter(s) : MIGUEL RISCO CASTILLO (Technological University of Peru, Lima , Peru)

Session Classification : Hardware Description of FPGA Development Platform: Nexys 2