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Joint ICTP-TWAS Latin-American Advanced Course on FPGA Design for Scientific Instrumentation | (smr 2384)

Friday 30 November 2012

**Sequential Logic Described in VHDL - A Design Example - - Room alternative: Conference
Hall (13:25-14:25)**

time	title	presenter
13:25	Sequential Logic Described in VHDL - A Design Example	RAUL ARTECHE DIAZ