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# International Training Workshop on FPGA Design for Scientific Instrumentation and Computing | (smr 2499)

## Wednesday 13 November 2013

### High Level Synthesis: how to improve FPGA design productivity - (09:00-10:00)

| time  | title   | presenter             |
|-------|---|-----------------------|
| 09:00 | High Level Synthesis: how to improve FPGA design productivity | FERNANDO RINCON CALLE |