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International Training Workshop on FPGA Design for Scientific Instrumentation and Computing | (smr 2499)

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Combinational and Sequential Logic Described in VHDL - Description, Synthesis and Simulation - (17:30-19:00)

time	title	presenter
17:30	Combinational and Sequential Logic Described in VHDL - Description, Synthesis and Simulation	KRISHNA M. KHARE, MIGUEL A. RISCO CASTILLO, CRISTIAN A. SISTERNA , CARLOS F. SOSA PAEZ