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International Training Workshop on FPGA Design for Scientific Instrumentation and Computing | (smr 2499)

Thursday 14 November 2013

Automated design of ASIP and accelerator based sub-systems for high-performance applications - (14:00-15:00)

time	title	presenter
14:00	Automated design of ASIP and accelerator based sub-systems for high-performance applications	LECH JOZWIAK